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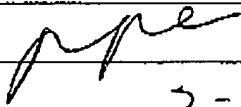
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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	09/823,751	
	Filing Date	April 3, 2001	
	First Named Inventor	E. Jennings Taylor et al.	
	Group Art Unit		
	Examiner Name		
Total Number of Pages in This Submission	3	Attorney Docket Number	461987-008C3

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## FAX COVER PAGE

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DATE: February 13, 2003

FROM: Mark P. Levy  
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TO: Examiner Bill Leader  
U.S. Patent and Trademark Office

PHONE:  
FAX: 703-872-9611

RE: U.S. Continuation Application Serial No. 09/823,751  
PULSE REVERSE ELECTRODEPOSITION FOR METALLIZATION AND  
PLANARIZATION OF SEMICONDUCTOR SUBSTRATES  
Our Ref. 461987-008C3

Attached for your reference find a copy of the Power of Attorney and Revocation of Prior Powers regarding the above-identified case. This paper will be filed formally with the U.S. Patent and Trademark Office as well.

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of Prior Powers (2pgs).

**Inventor(s):** E. Jennings Taylor et al.

**Title:** PULSE REVERSE ELECTRODEPOSITION FOR  
METALLIZATION AND PLANARIZATION OF  
SEMICONDUCTOR SUBSTRATES

**Serial No.:** 09/823,751

**Docket No.:** 461987-008-C3

**Due Date:** n/a

**Client Name:** Faraday Technology, Inc.  
mpl/ami

**Date Due** \_\_\_\_\_

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